

*JFW*PATENT
2936-0142P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: Hiroshi TAMEMOTO et al. Conf.: 2637

Appl. No.: 09/995,657 Group: 2116

Filed: November 29, 2001 Examiner: R. PERVEEN

For: SEMICONDUCTOR INTEGRATED CIRCUIT

LARGE ENTITY TRANSMITTAL FORMCommissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

September 28, 2004

Sir:

Transmitted herewith is a reply in the above-identified application.

The enclosed document is being transmitted via the Certificate of Mailing provisions of 37 C.F.R. § 1.8.

The enclosed document is being transmitted via facsimile.

The fee has been calculated as shown below:

	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST NUMBER PREVIOUSLY PAID FOR			PRESENT EXTRA	RATE	ADDITIONAL FEE
TOTAL	10	-	20	=	0	\$ 18	\$0.00
INDEPENDENT	10	-	10	=	0	\$ 86	\$0.00
<input type="checkbox"/> FIRST PRESENTATION OF A MULTIPLE DEPENDENT CLAIM						\$290	\$0.00
						TOTAL	\$0.00

Petition for () month(s) extension of time pursuant to 37 C.F.R. §§ 1.17 and 1.136(a). \$0.00 for the extension of time.

No fee is required.

Check(s) in the amount of \$0.00 is(are) enclosed.

Please charge Deposit Account No. 02-2448 in the amount of \$0.00. This form is submitted in triplicate.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By 
Terrell C. Birch, #19,382

b
TCB/CMV/jdm
2936-0142P

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Attachment(s)

(Rev. 02/08/2004)



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REPLY UNDER 37 C.F.R. § 1.111

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

September 28, 2004

Sir:

In reply to the Office Action dated June 28, 2004, the following remarks are respectfully submitted in connection with the above-identified application.

This reply includes Remarks.